

PROFILE

Name	Prof. Sushma S
Position & Affiliation	Assistant Professor, Department of ECE
Areas of Interest	VLSI Design,IOT, Embedded Systems, Image Processing.
Email	Sushma.ece@cambridge.edu.in
LinkedIn ID	www.linkedin.com/in/sushma-s-a4479b135
Google Scholar ID	https://scholar.google.com/citations?view_op=list_works&hl=id&user=mh3gESoAAAAJ
Orchid ID	https://orcid.org/0009-0003-7609-7339
Vidwan ID	664467
Scopus ID	
Professional Webpage (if any)	

Educational Qualifications:

ME	Dayananda Sagar College of Engineering, VTU	India	2017
BE	T John Institute of Technology, VTU	India	2014

Areas of Research:

VLSI Design,IOT, Embedded Systems, Image Processing.

Brief Profile: (write about yourself)

Prof.Sushma S I received my B.E. degree in Electronics and Communication Engineering from T. John Institute of Technology, Bengaluru, in 2014 and my M.Tech. degree in VLSI Design from Dayananda Sagar College of Engineering, Bengaluru, in 2017.

I am currently working as an Assistant Professor in the Department of Electronics and Communication Engineering at Cambridge Institute of Technology, Bengaluru. Prior to this, I served as an Assistant Professor at Maharaja Institute of Technology, Mysore, where I gained one year of teaching experience. I also have three years of industry experience as a Field Application Engineer at FTD Infocom, which provided me with valuable industrial exposure and practical expertise in electronics applications.

My areas of interest include VLSI Design, Embedded Systems, Internet of Things (IoT), Microcontrollers, Wireless Communication, Digital Electronics, and Artificial Intelligence applications in Electronics. I am passionate about delivering quality education, fostering innovation, and motivating students through practical and project-based learning.

To enhance my professional knowledge, I have participated in a five-day offline Faculty Development Program on **Introduction to Cyber Security** conducted at KSIT and a one-day Faculty Development

Program on **Advancements in Artificial Intelligence** conducted at MITM. I am committed to continuous learning and actively participate in academic and professional development activities to keep pace with emerging technologies.

I am a member of the **International Association of Engineers (IAENG)**. My objective is to contribute to academic excellence through effective teaching, student mentoring, research, and continuous professional development while preparing students to meet the evolving demands of the engineering profession.

Courses Taught: Wireless Communication Systems, Computer Organization with Microcontrollers, Introduction to Cyber Security, Microcontrollers,

Publications/Patents:

Publications	[1] Kiran Agarwal Gupta,Sushma,KS Guru Murthy “ 8-bit efficient digital PID controller architecture with parallelism ” 2017 2nd IEEE International Conference on Recent Trends in Electronics, Information & Communication Technology (RTEICT), 2018
Patents	
Book	

Research and Consultancy: