PROFILE

Name	Dr Priya Singh
Position & Affiliation	Assistant Professor, Department of ECE
Areas of Interest	VLSI Design, DFT, analog circuit design
Email	Priya.ece@cambridge.edu.in
LinkedIn ID	https://www.linkedin.com/in/dr-priya-singh-90066691/
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Scopus ID	57216270700
Professional Webpage (if any)	

Educational Qualifications:

Ph.D	Indira Gandhi delhi technical university for women	India	2024
MTech	Banasthali university	India	2015
BE	Rajiv Gandhi praudyogiki vishwavidyalaya	India	2012

Areas of Research:

Microelectronics, nanotechnology, analog circuit design, Design for testability of integrated circuits

Brief Profile: (write about yourself)

Received her B.E degree in Electronics and Communication Engineering from Rajeev Gandhi Proudyogiki Vishvavidyalaya Bhopal, M.Tech in VLSI Design from Banasthali University Jaipur and Ph.D. from Indira Gandhi Delhi Technical University For women.

Have around 2 years of experience in teaching and 7 years of research experience. She is currently working as an Assistant Professor in Department of Electronics and Communication Engineering, Cambridge Institute of Technology, Bangalore.

Have published 2 scopus indexed chapters in Elsevier and Taylor and Francis publication respectively. She had published 8+ journal papers which includes 3 SCIE/ESCI journals and Scopus and UGC indexed journals.

Had worked as Research Trainee With Bhabha Atomic Research Centre and worked on microelectronics based project.

Have organized events like National conferences, quiz for college .

Research areas include Analog and mixed signal design, Design for testability and robotics.

No. of Training Courses and Conference/Workshop/FDP/Webinars attended: 8
No. of Conference/Workshop/FDP/Webinars/Quiz Organized: 1

Courses Taught: B.Tech / M.tech (VLSI Design, Digital electronics, C, C++, Communication system, microprocessor, Analog and mixed signal circuit design, Digital Vlsi Design, ASIC, VHDL, Verilog)

Publications/Patents:

Publications	1. Priya Singh, Vandana Niranjan, Ashwni Kumar "10.56 Ghz Transimpedance Amplifier (Tia)using DynamicBody Bias Technique With Low Power Dissipation Of international Journal Of Electronics Engineering, Volume 11 Issue 2 Pp. 416-425 June 2019-dec 2019 (ISSN: 0973-7383)DOI .10.13140/RG.2.2.18760.26880 , **UGC listed** 2. Priya Singh, Vandana Niranjan, Ashwni Kumar , "Design and Simulation of Low Power Differential Transimpedance Amplifier Using Degenerations Capacitors" , Journal of nanoelectronics and optoelectronics, American scientific publishers, ISSN: 1555-130X (Print): EISSN: 1555-1318 (Online), Volume 17,pp-1-9 , **SCIE indexed** 3. Priya Singh, Vandana Niranjan, Ashwni Kumar , "Circuit World ,emarald publishing , ISSN: 0305-6120" , Accepted and published – july 2021 https://doi.org/10.1108/CW-06-2020-0102 **SCIE indexed** 4. P. Singh, V. Niranjan and A. Kumar, "Low Power Transimpedance Amplifier (TIA) Using Bulk Driven FVF," *2019 4th International Conference on Information Systems and Computer Networks (ISCON)*, Mathura, India, 2019, pp. 179-182, doi: 10.1109/ISCON47742.2019.9036259.

	5. P. Singh, V. Niranjan and A. Kumar, "Low Noise And Low Power Transimpedance Amplifier using Inverter Based Local Feedback," *2021 International Conference on Smart Generation Computing, Communication and Networking (SMART GENCON)*, Pune, India, 2021, pp. 1-5, doi: 10.1109/SMARTGENCON51891.2021.9645841. 6. Priya singh, vandana niranjan "Power And Wide Band BiCMOS Flipped Voltage Follower" 2nd International Conference on "*Recent Technological Developments in Electronics and Electrical Engineering-2018 (*RTDEEE-*2018)*" to be held at Jaipur Engineering College and Research Centre, Jaipur, during April 6-7, 2018. 7. Priya Singh,The concept paper on "On Chip High Voltage generator using Polysilicon Diodes" The International Journal Of Engineering And Science (IJES), Volume 4 Issue 1 January - 2015 Pages 40-43 ISSN (e): 2319 – 1813 ISSN (p): 2319 – 1805 Indexed in AMERICAN NATIONAL ENGINEERING DATABASE 8. Priya Singh, Ankita Rastogi, "VHDL implementation of DSDV Ad-Hoc Routing Protocol " , IOSR Journal of Mobile Computing & Application (IOSR-JMCA) e-ISSN: 2394-0050, P-ISSN: 2394-0042.Volume 1, Issue 2. (Nov - Dec. 2014), PP 07-11 Indexed in EUROPEAN engineering database
Patents	
Book/Book Chapters	1. *Priya Singh, Garima Singh, Gurjit Kaur , "*Green Smart Water and Sanitation System" , ,Book Green and Smart Technologies for Smart Cities ,Edition1st Edition ,Taylor n Francis publication, Published2019 ,ImprintCRC Press ,Pages22 ,eBook ISBN9780429454837 https://doi.org/10.1201/9780429454837 2. Priya Singh, Garima Singh, Gurjit Kaur, Chapter 17 - Integrating Artificial Intelligence/Internet of Things Technologies to Support Medical Devices and Systems, Editor(s): Gurjit Kaur, Pradeep Tomar, Marcus Tanque, Artificial Intelligence to Solve Pervasive Internet of Things Issues,Academic Press, Elsevier 2021,Pages 331-349, ISBN 9780128185766, https://doi.org/10.1016/B978-0-12-818576-6.00017-4.
