

PROFILE

Name	K V S V Trinadh Reddy
Position & Affiliation	Associate Professor, Department of ECE
Areas of Interest	Analog Electronics, Signals & Systems, DSP, DIP, Network Analysis
Email	trinadhreddy.ece@cambridge.edu.in
LinkedIn ID	https://www.linkedin.com/in/trinadh-reddy-koyya
Google Scholar ID	https://scholar.google.com/citations?hl=en&user=nieRzQwAAAAJ
Orchid ID	0009-0001-8758-4613
Vidwan ID	676359
Scopus ID	57422605200
Professional Webpage (if any)	https://www.youtube.com/@GOLDENHOUR4YOU/videos

Educational Qualifications:

Ph.D.	S V University, Tirupati, Andhra Pradesh	India	2025
M.E	Andhra University, Visakhapatnam, A.P	India	2003
B.Tech	RGM CET, Nandyal, J N T University, A.P	India	1999

Areas of Research:

Digital Image Processing, AIML and IoT

Brief Profile:

Dr. K V S V Trinadh Reddy is an Associate Professor in the Department of Electronics and Communication Engineering at Cambridge Institute of Technology, K. R. Puram, Bengaluru. He is a highly dedicated and committed academician with over 22 years of rich teaching experience, spanning both online and offline modes of instruction.

He has taught core engineering subjects such as Analog Electronics, Control Systems, Signals & Systems, and Network Analysis, and has mentored more than 80,000 students preparing for GATE and Engineering Services examinations. His teaching outreach has been delivered through reputed platforms and institutions including MADE EASY and Unacademy, earning him wide recognition among students nationwide.

With extensive academic and professional experience in engineering education, research, and student development, Dr. Reddy has made significant contributions in the areas of Digital Image Processing and emerging technologies such as Internet of Things (IoT) and Artificial Intelligence & Machine Learning. Driven by a strong passion for student growth, innovation, and academic excellence, he continuously adopts advanced teaching–learning methodologies, actively engages in mentoring and guidance, and contributes meaningfully to institutional development initiatives.

Courses Taught:

- Network Analysis, Electronic Devices and Circuits/ Basic Electronics, Analog Electronic Circuits, Pulse and Digital Circuits, Linear Integrated Circuits, Digital Logic Design, Digital system design using Verilog, Microprocessors and Interfacing, Control Systems, Signals and Systems, Digital Signal Processing, Analog Communication, Microwave Engineering, Optical Communication, Electronic Measurement and Instrumentation, Analog IC Design (M.Tech), CMOS RF Design (M.Tech), Digital Systems & logic Design (M.Tech), CAD for Signal Processing (M.Tech) and Digital Signal Compression (M.Tech).

Laboratories handled:

NA, EDC, AEC, LIC, Signals and Systems, DSP, DLD, HDL, Microprocessors, Analog Communication, MWE and Optical Communication

Publications/Patents:

Publications

1. KSVS Trinadh Reddy and S. Narayana Reddy. **“Digital Image Watermarking Techniques: A Review”** International Journal of Advanced Research in Engineering and Technology (IJARET) Volume 12, Issue 4, April 2021, pp.105-112 Article ID: IJARET_12_04_013, DOI: 10.34218/IJARET.12.4.2021.013, **(Scopus - Indexed)**
2. KSVS Trinadh Reddy and S. Narayana Reddy. **“An improved medical image watermarking technique based on Weber’s law descriptors.** Traitement du Signal, Vol. 38, No. 6, pp. 1637-1646. <https://doi.org/10.18280/ts.380607> **(Scopus - Indexed)**
3. KSVS Trinadh Reddy and S. Narayana Reddy. **“A Novel Blind Double-Colour Image Watermarking Algorithm Utilizing Walsh–Hadamard Transform with Symmetric Embedding Locations”**. Symmetry 2024, 16, 877.<https://doi.org/10.3390/sym16070877> **(SCIE-Indexed)**
4. S Hemalatha, KSVS Trinadh Reddy, Tavanam Venkata Rao, T Ramaswamy, N Muthuvairavanpillai and G Krishna Mohan.” **Advancing Autonomous Systems: A Review of Emerging Trends in Robotics** “Journal European des Systemes Automatices, Volume 58, No: 5, May 2025, pp. 931- 921. <https://doi.org/10.18280/jesa.580505> **(Scopus - Indexed)**
5. S Hemalatha, KSVS Trinadh Reddy, Ramaswamy T, RVV Krishna, P Supriya and S N Ananthi “Enhancing **“MANET Security Using AI-Driven Intrusion Detection Systems”** Journal of Communications, Vol. 20, No. 4, pp. 371-383. **(Scopus - Indexed)**

	6. S Hemalatha, KVS V Trinadh Reddy, Tavanam Venkata Rao, Ramaswamy T, Priti Shende, Naga Malleshwari “Hybrid Lightweight Cryptographic Framework for Enhancing Security and Efficiency in Healthcare Wireless sensor Networks” Journal of Communications, Vol. 15, September 2025, No. 9, pp. 1797-1807. https://doi.org/10.18280/ijssse.150904 (Scopus - Indexed)
Patents	System and Method for server-based eSIM subscription management in carrier-locked devices
Book chapters	Deep learning-Enhanced Association rule Mining for Disease Diagnosis and treatment Planning
Workshops and training programs Attended	• Academic Leadership program-PESIT, Bangalore, Karnatak (March 2012) • Two-day Workshop on Labview-Cambridge Institute of Technology, Bangalore, Karnataka (Feb 2012) • Digital Image Processing- Cambridge Institute of Technology, Bangalore, Karnataka (Dec 2011) • CMOS RF Design-REVA Institute for Engineering and Technology, Bangalore, Karnataka (Aug 2010) • Faculty Development Program-East Point college of Engineering and Technology, Bangalore, Karnataka (July 2010) • Applications of Electronic Design Automation (EDA) Tools in Chip Design Vasavi Engineering College- Hyderabad, A.P (Nov 2009) • Computer Communication Networks and Protocols-SNIST Hyderabad, A.P (May 2008) • Image and Video Processing- SNIST, Hyderabad, A.P (June 2007) • Functional Approach to VLSI Design-GNITS, Hyderabad, A.P (Feb 2007) • TCP /IP Networks & Protocols-JNTU, Hyderabad, A.P (Dec 2006) • Analog IC Design-NIT, Warangal, A.P (June 2006) • Digital IC Design-NIT, Warangal, A.P (Feb 2006)
Workshops Conducted	3-Day workshop on “IoT and Embedded Systems Applications using ESP32 Microcontroller” at Cambridge Institute of Technology (September 2025)  

