

## Vaman B Gudi

#302, Siri Chowdeshwari Enclave  
29<sup>th</sup> Main, 48<sup>th</sup> Cross  
PP Layout, Uttarahalli  
Bangalore-61



Mobile No. +919448233711  
Email: vamanbgudi@gmail.com

### Objectives:

- To take up challenges in the work related area by utilizing my potential skills.
- To work with determination & Initiation.
- To prove as a valuable asset in the organization.

### Areas of Experience:

Have **Twenty Two** years of experience in (18 years Teaching and 4 Years in administration).

- **Registered for PhD at JNTU, Hyderabad. Satisfactorily completed all RRM's and writing thesis for submission.**

- **Responsibilities:**

- Worked as Board of Examiner for Kuvempu University.
- Worked as Dean Academics at SDMIT Ujire.
- Visvesvaraya Technological University Local Enquiry Committee (LIC) member.
- Worked as Chief Coordinator for NMDC at SDMIT Ujire.
- Worked as Chief Coordinator for AICTE approval process for 2010-11, 2011-12, and 2012-13.
- Worked as Governing Council Member for S. D. M. Institute of Technology, Ujire.
- Worked as, Head of the Department of Computer Science & Engineering.
- Worked as AICTE affiliation coordinator.
- Worked as, Head of the department of Electronics and Communication Engineering.
- Worked as, In-charge Principal.
- Working as, Special Officer for Academic and DPAR sections of Visvesvaraya Technological University, Belagavi, Karnataka.
- Working as, Nodal Officer for SAKAL Scheme of Government of Karnataka.
- Working as, Nodal Officer for online Students Grievance Redressal Portal.
- Worked as, Special Invitee for Uniform Affiliation Committee of Department of Higher Education, Government of Karnataka.
- Member of Governing Council to SDMCET, Dharwad as AICTE's nominee.
- Worked as Coordinator for making University application to TEQIP-III programme.
- Worked as Coordinator for 12- B activities in VTU.
- Handled Subjects like **VLSI Design, Microprocessors, Logic Design, Electronics Circuits, Solid State Devices, VHDL & Verlog, VLSI Design Verification.**

Sl.No.	Exam. Passed	University/ Institution	Year of Passing	Main Subjects	Division or Equivalent
1.	B.E.	Karnataka University, Dharawad	1995	Electronics & Communication	First Class
2.	M.Tech.	VTU, Belgaum	2006	Digital Electronics	First Class with Distinction
3.	P.hD.	JNTU, Hyderabad	Theses will be submitted shortly	VLSI	

• **Professional Experience Summary:**

Sl.No.	Name & Address of the employer/ Institution	Period of Service		Designation of Post held and scale of pay	Nature of work and Level of responsibility
		From	To		
1.	S.J.M.I.T., Chitradurga, Karnataka	01-07-1995	31-08-2003	Lecturer	Teaching
2.	S.J.M.I.T., Chitradurga, Karnataka	01-09-2003	31-07-2005	Sr. Lecturer	Teaching and Project Coordinator
3.	S.J.M.I.T., Chitradurga, Karnataka	01-08-2005	31-08-2006	Asst. Professor	Teaching and Project Coordinator
4.	S.J.M.I.T., Chitradurga, Karnataka	01-09-2006	26-07-2008	Professor	Teaching and Project Coordinator
5.	S.D.M. Institute of Technology, Ujire, Karnataka	28-07-2008	06-07-2012	Professor & Head	Teaching, Administration, Dean (Academics)
6.	M.I.T.E., Moodbidri, Karnataka	08-07-2012	30-03-2013	Professor & Head	Teaching, Administration, Controller of Examination
7.	Visvesvaraya Technological University, Belagavi, Karnataka	01-04-2013	Till date	Special Officer(Academic & DPAR)	Administration

**Technical Conferences & Publications:**

1. Presented a paper on "Performance analysis of Synchronous Buck Converter" in the Knowledge Utsav, a National conference on Power Electronics held at Jain University, Bangalore on 28<sup>th</sup> August 2010. Received best paper award of the conference.

2. Presented a paper on “**Performance of Single Stage Synchronous Buck Converter with Compensator Network** ” at International conference on Current Trends in Engineering & Management ICCTEM 2012, held at VVCE, Mysore during July 12-14, 2012.
3. Published paper “**Design Issues in Single Stage Synchronous Buck Converter**” in International journal IJEATE (International Journal of Emerging Technology and Advanced Engineering), Volume 2, Issue 4, April 2012, Page No’s: 452-456.

### Course Attended:

1. Short term courses on DSP and Motorola Microcontrollers held at BMSE, Bangalore.  
**Duration:** One Week.  
Learnt and hands on knowledge about DSP processors and Motorola microcontrollers.
2. Participated in a Workshop on “**VHDL Programming**” conducted by Department of Electronics & Communication, at Gousia Engineering College, Ramanagara, Bangalore.  
**Duration:** Three Days.
3. Participated in a workshop on “**Tanner Tools**” at S.J.C.E, Mysore.  
Learnt about the VLSI tools.
4. Selected and participated in **BIET, Davanagere** for a course on “**Microwind Tool**”.
5. One Day workshop on “**Mat Lab and its Applications**” conducted by Crains Technology, Bangalore.
6. Two Days Workshop on “**Research Trends in Electronics & Communication Engineering**” at SDMCET, Dharwad.

### Coordinated:

1. Worked as a Coordinator in a Faculty Development Programme on “**DSP & its Applications**“, held on 26<sup>th</sup> to 28<sup>th</sup> July 2010 at SDMIT, Ujire, Karnataka.
2. Worked as a Coordinator in a National Level Workshop on “**Engineering Pedagogy**“, held on 29<sup>th</sup> & 31<sup>th</sup> July 2010 at SDMIT, Ujire, Karnataka.

### Personal Details:

**Date of Birth** : 14<sup>th</sup> December 1970

**Permanent Address** : s/o B S Gudi  
Gudi Oni,  
Haveri, Karnataka,  
INDIA – 581110.

**Phone Number** : 08375-233110, +919448233711

**Passport No.** : G4347658

**Membership** : MISTE. LM 20868

### References:

1. **Dr. Karisiddappa, Hon’ble Vice-Chancellor, VTU, Belagavi**
2. **Dr. H.N. Jagannatha Reddy, Registrar, VTU, Belagavi**
3. **Dr. D.H.Rao, Former Dean, Faculty of Engineering, VTU Belagavi**

  
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