

Name	Dr. RAVI KUMAR.M
Position & Affiliation	Associate Professor, Department of ECE
Areas of Interest	VLSI Design & Embedded Systems
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Educational Qualifications:

Ph.D	VTU, Belagavi	India	2024
MTech	SJCE Mysore, VTU - Belagavi	India	2007
BE	PDIT, VTU - Belagavi	India	2005

Areas of Research: VLSI, CMOS VLSI ,AMVLSI, Microcontrollers

Brief Profile:

Has Completed PhD in CMOS VLSI domain at VTU, Belagavi, M.Tech from SJCE, Mysore and B.E (ECE) from PDIT, Hospet. He has over Eighteen years of Industry & Academic Experience. The areas of interest are VLSI, CMOS VLSI, AMVLSI, Embedded Systems. Associated with CiTech, K.R.Puram from **Aug 2010 onwards till date.**

Guiding students at various levels (BE, MTech): Guided 28 UG Projects, 15 UG Mini project, 2PG projects.

- ❖ **Awards/Achievements/Others:** Qualified GATE-2005 with 94.16 Percentile,
 - Awarded as Best Teacher for the academic year 2023-24.

Courses Taught: VLSI, CMOS VLSI , AMVLSI, Microcontrollers, AEC, DSS, RTS,RTOS,OS, EDC, LIC, Wireless communication, IoT, WC 4G LTE.

Publications/Patents:

Publications	<p><u>Papers Published</u></p> <ol style="list-style-type: none"> 1. Title: Integrating Genetic Algorithm and Leverage the Slap Swarm Algorithm to Optimize the LSTM Model to Predict Cardiovascular Disease Indexing: Web of science DOI: https://doi.org/10.63001/tbs.2024.v19.i Year:2024 Journal: Thebioscan.
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2. Title: Dimensionality Reduction in Animal Movement Data Using Linear Algebra and Machine Learning
Indexing: Web of science
Year:2024
Journal: Thebioscan.
3. Title: Mathematical Applications and Modelling in Higher Education
Indexing: Scopus
DOI: <https://doi.org/10.53555/kuey.v30i7.6665>
Year: 2024
Journal: Educational Administration: Theory and Practice.
4. Title: Energy efficient resource allocation in cognitive radio networks using self adaptive Harris Hawks Optimization Algorithm
Indexing: Scopus
DOI:
Year: 2024
Conference: International Conference on Wireless Communication and Internet of Everything - Springer
5. Title: Simulation aspects of low power energy efficient phase frequency detector employed in PLL-45nm CMOS process
Indexing: Scopus
DOI: <https://romanpub.com/jaetv5-4-2023.php>
Year: 2023
Journal: International Journal of Applied Engineering & Technology (IJAET)
6. Title: Phase Locked Loop Design and Analysis for 45nmCMOS
Indexing: Google Scholar
<https://www.ijstm.com/currentissue3.php?id=3308>
Year:2023
Journal: International Journal Of Science Technology & Management
7. Title: A comprehensive study on Design Implementation of CMOS Low Noise Amplifier
Indexing: Google Scholar
DOI: 10.37897.GRJ.2022.V8I12.22.50719
<https://gradivareview.com/volume-9-issue-1-2023/>
Year: 2023
Journal: Gradiva Review Journal
8. Title: Review Paper On Operation Of Vital Health Monitoring Wearable
ISBN-978-81-955351-0-1
Year: 2022
Journal: NCETE-22
9. Title: Vocal Eyes Gaze Communication System
ISBN: 978-81-955351-0-1
Year: 2022

	<p>Journal: NCETE-22</p> <p>10. Title: Design and Analysis of Improved 3-Stage LNA Architecture for CMOS RF Front End Receiver Systems Indexing: Scopus (Q2) DOI: 10.22266/ijies2022.0630.02 Year: 2021 Journal: International Journal of Intelligent Engineering and Systems</p> <p>11. Title: An Efficient Design Implementation Of OFDM Based Transmission System. Indexing: Scopus https://www.ijstr.org/research-paper-publishing.php?month=july2019 Year: 2019 Journal: International Journal of Scientific & Technology Research</p> <p>12. Title: Smart vehicle prototype of Arduino based Vehicle Indexing: Google Scholar Year: 2016 Journal: IRAJ</p> <p>13. Title: Home Automation Prototype using IoT Indexing: Google scholar Year: 2016 Journal: International Journal of Engineering Research</p>
<p>Patents</p>	<p><u>Patents Published</u></p> <p>1. Patent Application No: 417885-001 Cbr Number:208948 Cbr Date, time:24/05/2024, 17:47:12 Applicant Name: Dr.Ravi kumar.M Design Application Status:Design Accepted and Published, Journal No 27/2024 & Journal date:05/07/2024.</p> <p>2. Patent Application No: 422816-001 Cbr Number:212109 Cbr Date, time:10/07/2024, 19:23:22 Applicant Name: Dr.Ravi kumar.M Design Application Status: Design Accepted and Published, Journal No is 36/2024 and Journal Date is 06/09/2024.</p> <p>3. Patent Application No: 424826-001 Cbr Number:213256 Cbr Date, time:26/07/2024, 16:50:23 Applicant Name: Dr.Ravi kumar.M Design Application Status: Design Accepted and Published, Journal No is 37/2024 and Journal Date is 13/09/2024.</p>

	<p>4. Application Number: 430404-001 Cbr Number: 216697 Cbr Date: 13/09/2024 16:41:53 Applicant Name: Dr. RAVI KUMAR M Design Application Status: Design Accepted and Published, Journal No is 43/2024 and Journal Date is 25/10/2024.</p>
Book/Book Chapters	<p>1. ISBN : 978-93-340-5199-5 Publisher: Raja Rammohun Roy National Agency for ISBN Department of Higher Education, Ministry of Education Government of India.</p> <p>2. IGI GLOBAL (SCOPUS) Book chapter on "Edge accessibility computing and block chain assisted solution for smart cities application" is Published.</p>